

SBR2A40P1

2.0A SBR[®] Surface Mount Super Barrier Rectifier PowerDI™123

Features

- Low Forward Voltage Drop
- Low Leakage Current
- Superior Reverse Avalanche Capability
- Excellent High Temperature Stability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- ±16KV ESD Protection (HBM, 3B)
- ±25KV ESD Protection (IEC61000-4-2 Level 4, Air Discharge)
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q 101 Standards for High Reliability

Mechanical Data

- Case: PowerDI™123
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Marking Information: See Page 4Ordering Information: See Page 4

Maximum Ratings @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage	V _{RRM} V _{RWM}	40	V
DC Blocking Voltage	V_{RM}		
RMS Reverse Voltage	$V_{R(RMS)}$	28	V
Average Rectified Output Current (See Figure 1)	Io	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	50	А
Maximum Thermal Resistance Thermal Resistance Junction to Soldering (Note 2) Thermal Resistance Junction to Ambient (Note 3) Thermal Resistance Junction to Ambient (Note 4)	R _{eJS} R _{eJA} R _{ejA}	5 180 115	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Notes:

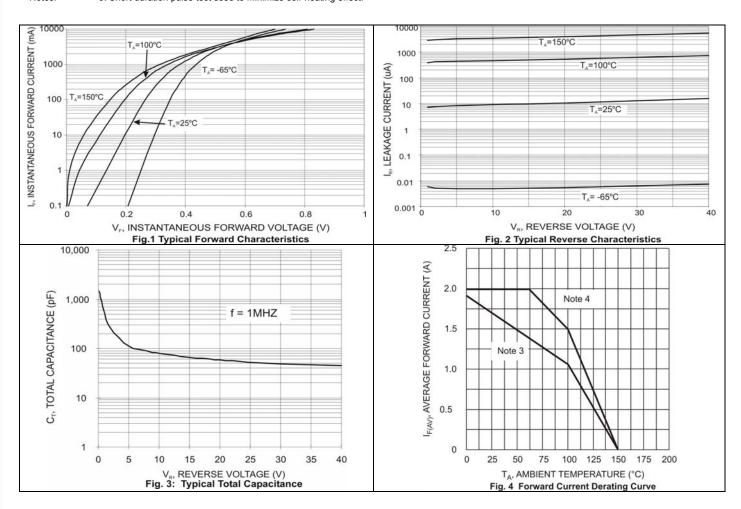
- 1. RoHS revision 13.2.2003. High temperature solder exemption applied, see EU Directive Annex Note 7.
- 2. Theoretical R_{eJS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.



Electrical Characteristics @ T_A = 25°C unless otherwise specified

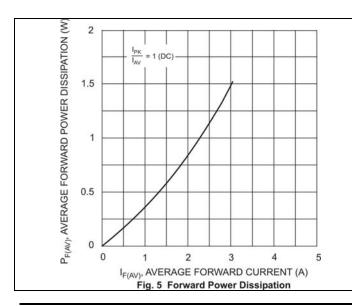
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
Reverse Breakdown Voltage (Note 5)	$V_{(BR)R}$	40	-	-	V	I _R = 100 μA		
Forward Voltage Drop	V _F	- - - - -	0.265 0.38 0.45 0.17 0.325 0.42	0.315 0.43 0.50 0.22 0.375 0.47	٧	$\begin{split} I_F &= 0.1A, T_J = 25^{\circ}\text{C} \\ I_F &= 1.0A, T_J = 25^{\circ}\text{C} \\ I_F &= 2.0A, T_J = 25^{\circ}\text{C} \\ I_F &= 0.1A, T_J = 125^{\circ}\text{C} \\ I_F &= 1.0A, T_J = 125^{\circ}\text{C} \\ I_F &= 2.0A, T_J = 125^{\circ}\text{C} \\ \end{split}$		
Leakage Current (Note 5)	I _R	-	8 16 1.3 2.1	40 100 8 10	μΑ μΑ mA mA	$V_R = 5V, T_J = 25 \text{ °C}$ $V_R = 40V, T_J = 25 \text{ °C}$ $V_R = 5V, T_J = 125 \text{ °C}$ $V_R = 40V, T_J = 125 \text{ °C}$		

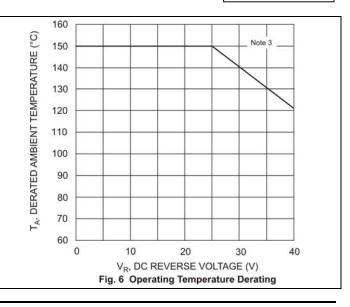
Notes: 5. Short duration pulse test used to minimize self-heating effect.



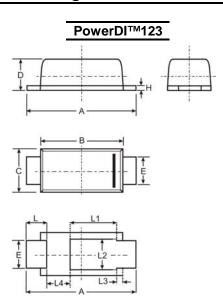


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Package Outline Drawings



PowerDI [™] 123										
Dim	Min	Max	Тур							
Α	3.65	3.75	3.70							
В	2.775	2.825	2.80							
С	1.750	1.800	1.775							
D	D 0.955		0.98							
E	0.95	1.05	1.00							
Н	0.15	0.25	0.20							
L	L 0.60		0.65							
L1 —		_	1.36							
L2	_	_	1.10							
L3		_	0.20							
L4	0.95	1.25	1.05							
All Dimensions in mm										



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Marking, Polarity, Weight & Ordering Information

2	Case	Style	Marking	Weight		
SBR2A40F	Top View	Back View	[[2A4 ₹]	0.096g (approx.)		

Ordering Information	Date Code
SBR2A40P1-7 3000/Tape & Reel	2A4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

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Year	2006		2007		2008		2009		2010	2	2011	20	12	
Code	Т		U		V		W		X		Y		Z	
N	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
(Code	1	2	3	4	5	6	7	8	9	0	N	D	

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