

Vishay General Semiconductor

Glass Passivated Ultrafast Rectifier



FEATURES

- · Superectifier structure for high reliability condition
- · Cavity-free glass-passivated junction
- Ultrafast reverse recovery time
- Low forward voltage drop
- · Low leakage current
- Low switching losses, high efficiency
- · High forward surge capability
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer and telecommunication.

MECHANICAL DATA

Case: GP20, molded epoxy over glass body Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade Base P/NHE3 - RoHS compliant, AEC-Q101 gualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL	EGP50A	EGP50B	EGP50C	EGP50D	EGP50F	EGP50G	UNIT	
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	V	
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	V	
Maximum DC blocking voltage	V _{DC}	50	100	150	200	300	400	V	
Maximum average forward rectified current 0.375" (9.5 mm) lead length at T_L = 55 °C	I _{F(AV)}	5							
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	150						А	
Operating and storage temperature range	T _J , T _{STG}	- 65 to + 150						°C	

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PRIMARY CHARACTERISTICS 5.0 A I_{F(AV)} 50 V to 400 V V_{RRM} 150 A I_{FSM} 50 ns t_{rr} 0.95 V, 1.25 V V_{F} 150 °C T_J max.



Revision: 15-Mar-11





RoHS COMPLIANT

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)										
PARAMETER	TEST CONDITIONS		SYMBOL	EGP50A	EGP50B	EGP50C	EGP50D	EGP50F	EGP50G	UNIT
Maximum instantaneous forward voltage	5.0 A		V _F	0.95			1.25		V	
Maximum DC reverse current		T _A = 25 °C	1-	5.0						μA
at rated DC blocking voltage		T _A = 125 °C	IR			5			μΛ	
Maximum reverse recovery time	$I_{\rm F} = 0.5$ A, $I_{\rm R} = 1.0$ A, $I_{\rm rr} = 0.25$ A		t _{rr}	50					ns	
Typical junction capacitance	4.0 V, 1 MHz		CJ	95		7	5	pF		

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)								
PARAMETER	SYMBOL	EGP50A	EGP50B	EGP50C	EGP50D	EGP50F	EGP50G	UNIT
Turnical thermal registeries	R _{0JA} ⁽¹⁾	20						°C/W
Typical thermal resistance	R _{0JL} ⁽¹⁾	5.0						0/10

Note

⁽¹⁾ Thermal resistance from junction to ambient, and from junction to lead at 0.375" (9.5 mm) lead length, PCB mounted

ORDERING INFORMATION (Example)									
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE					
EGP50G-E3/54	1.01	54	1400	13" diameter paper tape and reel					
EGP50G-E3/73	1.01	73	1000	Ammo pack packaging					
EGP50GHE3/54 (1)	1.01	54	1400	13" diameter paper tape and reel					
EGP50GHE3/73 ⁽¹⁾	1.01	73	1000	Ammo pack packaging					

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

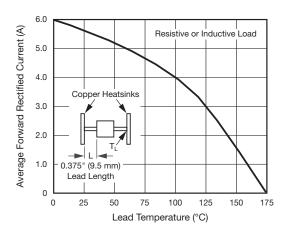


Fig. 1 - Maximum Forward Current Derating Curve

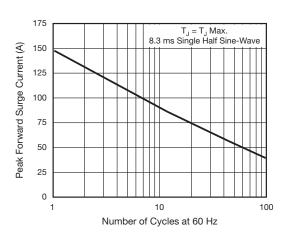


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

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EGP50A thru EGP50G

Vishay General Semiconductor

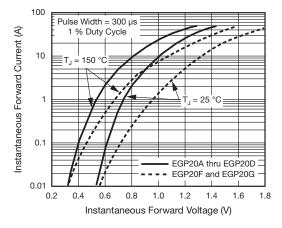


Fig. 3 - Typical Instantaneous Forward Characteristics

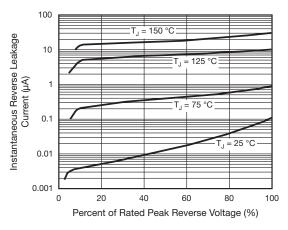


Fig. 4 - Typical Reverse Leakage Characteristics

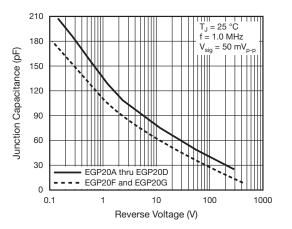


Fig. 5 - Typical Junction Capacitance

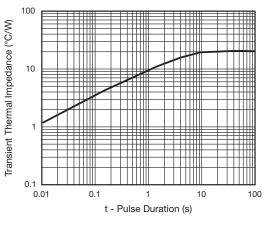
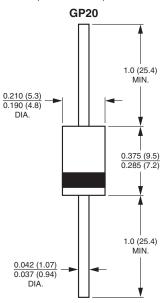


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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