

SURFACE MOUNT SUPER FAST RECTIFIERS

ER2A THRU ER2J

50V-600V 2.0A

FEATURES

- For surface mounted applications
- Low profile package
- Built-in strain relief
- Easy pick and place
- Superfast recovery times for high efficiency
- Plastic package has Underwriters Laboratory
 Flammability Classification 94V-O
- Glass passivated junction
- High temperature soldering:
 260 ¢J/10 seconds at terminals

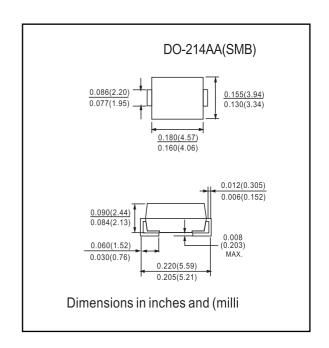
MECHANICAL DATA

Case: JEDEC DO-214AA molded plastic Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

Polarity: Indicated by cathode band

Standard packaging: 12mm tape (EIA-481)

Weight: 0.003 ounce, 0.093 gram



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Characteristic		Symbol	ER2A	ER2B	ER2C	ER2D	ER2E	ER2G	ER2J	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		Vrrm Vrwm Vr	50	100	150	200	300	400	600	٧
RMS Reverse Voltage		VR(RMS)	35	70	105	140	210	280	420	٧
Average Rectified Output Current	@T _L = 110°C	Ю	2.0						Α	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)		İFSM	50							Α
Forward Voltage	@I _F = 2.0A	VFM	0.95			1.25		1.7	V	
Peak Reverse Current At Rated DC Blocking Voltage	@T _A = 25°C @T _A = 100°C	IRM	5.0 500						μА	
Reverse Recovery Time (Note 1)		trr	35							nS
Typical Junction Capacitance (Note 2)		Cj	25							pF
Typical Thermal Resistance (Note 3)		R_{θ} JL	20							K/W
Operating and Storage Temperature Range		Tj, Tstg	-65 to +150							°C

Note: 1. Measured with $I_F = 0.5A$, $I_R = 1.0A$, $I_{rr} = 0.25A$,

2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

3. Mounted on P.C. Board with 8.0mm² land area.



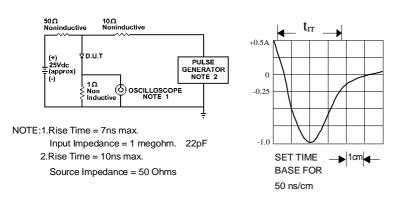
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RATINGS AND CHARACTERISTIC CURVES

ER2A **THRU** ER2J



SINGLE PHASE HALF WAVE RESISTIVE OR INDUCTIVE AVERAGE FORWARD **CURRENT AMPERES** P.C.B MOUNTED ON **PAD AREAS** 50 75 100 125 150 175 LEAD TEMPERATURE, ¢J

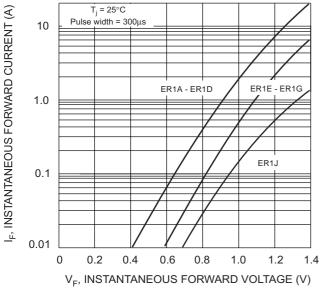
Fig. 2-MAXIMUM AVERAGE FORWARD **CURRENT RATING**

Pulse width

8.3 ms single half-sine-way
(JEDEC method)

Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND

TEST CIRCUIT DIAGRAM



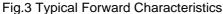
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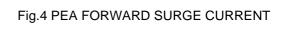
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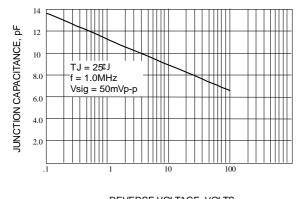
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I_{FSM}, PEAK FORWARD SURGE CURRENT (A)





NUMBER OF CYCLES AT 60Hz



REVERSE VOLTAGE, VOLTS

Fig. 5-TYPICAL JUNCTION CAPACITANCE

100