

# ESH1B, ESH1C, ESH1D

HALOGEN

FREE

# Vishay General Semiconductor

## Surface Mount Ultrafast Plastic Rectifier



DO-214AC (SMA)

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	1.0 A				
V <sub>RRM</sub>	100 V, 150 V, 200 V				
t <sub>rr</sub>	25 ns				
V <sub>F</sub>	0.90 V				
T <sub>J</sub> max.	175 °C				

## **FEATURES**

- · Low profile package
- · Ideal for automated placement
- · Glass passivated chip junction
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power loss
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

### **TYPICAL APPLICATIONS**

For use in secondary rectification and freewheeling for ultrafast switching speeds AC/AC and DC/DC converters in high temperature conditions for both consumer applications.

### **MECHANICAL DATA**

Case: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	ESH1B	ESH1C	ESH1D	UNIT	
Device marking code		EHB	EHC	EHD		
Maximum repetitive peak reverse voltage	$V_{RRM}$	100	150	200	V	
Maximum RMS voltage	V <sub>RMS</sub>	70	105	140	V	
Maximum DC blocking voltage	$V_{DC}$	100	150	200	V	
Maximum average forward rectified current at $T_L = 150  ^{\circ}\text{C}$	I <sub>F(AV)</sub>	1.0			Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	I <sub>FSM</sub>	50			А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 175			°C	

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	VALUE	UNIT		
Maximum instantaneous forward	I <sub>F</sub> = 0.7 A		V <sub>F</sub> <sup>(1)</sup>	0.87	V		
voltage	I <sub>F</sub> = 1 A		V <sub>F</sub>	0.90			
Maximum DC reverse current at rated DC blocking voltage		T <sub>A</sub> = 25 °C	I <sub>R</sub>	1.0	μА		
		T <sub>A</sub> = 125 °C		25			
Maximum reverse current	V <sub>R</sub> = 20 V, T <sub>J</sub> = 150 °C		I <sub>R</sub>	50	μΑ		
Maximum reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	25	ns		
Typical reverse recovery time	$I_F = 0.6 \text{ A}, V_R = 30 \text{ V},$ $dI/dt = 50 \text{ A/}\mu\text{s}, I_{rr} = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C	- t <sub>rr</sub>	25	ns		
		T <sub>J</sub> = 100 °C		35			
Typical stored charge	$I_F = 0.6 \text{ A}, V_R = 30 \text{ V},$ $dI/dt = 50 \text{ A/}\mu\text{s}, I_{rr} = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C	Q <sub>rr</sub>	10	nC		
		T <sub>J</sub> = 100 °C		15			
Typical junction capacitance	4.0 V, 1 MHz		CJ	25	pF		

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	ESH1B	ESH1C	ESH1D	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)	85			°C/W	
	R <sub>0JL</sub> (1)	30				

### Note

<sup>(1)</sup> Units mounted on PCB with 5.0 mm x 5.0 mm (0.013 mm thick) land areas

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ESH1D-M3/61T	0.064	61T	1800	7" diameter plastic tape and reel	
ESH1D-M3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

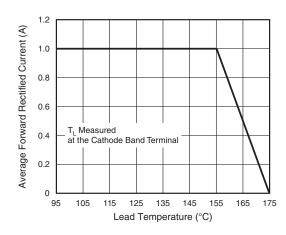


Fig. 1 - Maximum Forward Current Derating Curve

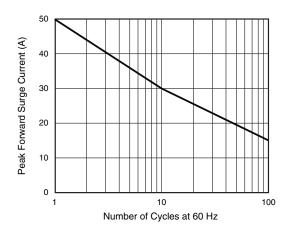


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

100



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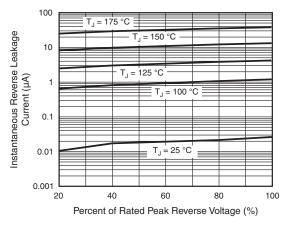
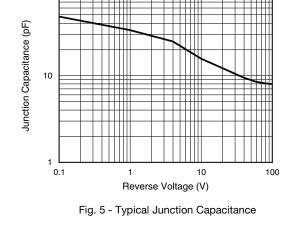


Fig. 3 - Typical Reverse Leakage Characteristics



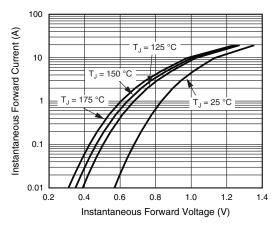


Fig. 4 - Typical Instantaneous Forward Characteristics

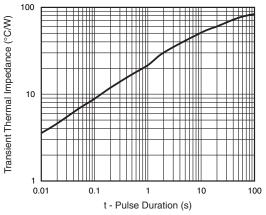
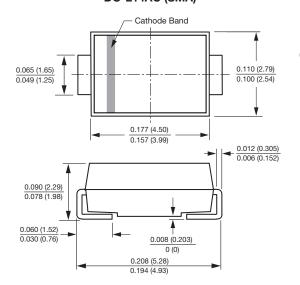
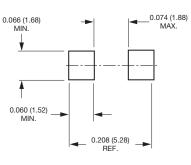


Fig. 6 - Typical Transient Thermal Impedance

## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters) DO-214AC (SMA)





**Mounting Pad Layout** 

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